

## IN THE CLAIMS

Please amend the claims as indicated by the revision marks and status below:

1. (CURRENTLY AMENDED) An apparatus comprising:

a hybrid network having a hybrid input, a receive input, and a hybrid output, wherein the receive input is capacitively coupled to a subscriber line carrying an upstream data signal and a downstream data signal;

a driver providing the upstream data signal to the subscriber line and the hybrid input, wherein the driver is capacitively coupled to the hybrid input, wherein the hybrid output provides the extracted downstream data signal from the subscriber line, wherein the capacitive coupling from the receive input to the subscriber line is distinct from the capacitive coupling from the hybrid input to the driver.

2. (ORIGINAL) The apparatus of claim 1 wherein the hybrid network resides on an integrated circuit die.

3. (ORIGINAL) The apparatus of claim 2 wherein the driver resides on the same integrated circuit die.

4. (ORIGINAL) The apparatus of claim 1 wherein the hybrid network is a complementary metal oxide semiconductor integrated circuit.

5. (ORIGINAL) The apparatus of claim 1 wherein the upstream and downstream data signals are multitone modulated data signals.

6. (CURRENTLY AMENDED) An apparatus, comprising:

a hybrid network having a receive port capacitively coupled to receive a composite signal including an upstream data signal and a downstream data signal communicated on a subscriber line, the hybrid network having a hybrid

input port capacitively coupled to receive the upstream data signal from a driver, the hybrid network providing the downstream data signal at an output port, wherein the capacitive coupling from the receive port to the subscriber line is distinct from the capacitive coupling from the hybrid input port to the driver, wherein the hybrid network order is less than or equal to 2.

7. (PREVIOUSLY PRESENTED) The apparatus of claim 6 wherein a transfer function from the driver to the receive port is  $\frac{Z(s)}{R_D + Z(s)}$ , wherein  $R_D$  is a driver output impedance wherein  $Z(s)$  is a subscriber line impedance, wherein a transfer function from the receive port to the output port is  $K_{rx} \cdot \frac{s}{s + HYB0}$ , wherein HYB0 is programmatically adjustable, wherein  $K_{rx}$  is a receive path gain.

8. (PREVIOUSLY PRESENTED) The apparatus of claim 7 wherein a transfer function from the hybrid input port to the hybrid output port is  $K_{HYB} \cdot \frac{s}{s + HYBP}$ , wherein HYBP is programmatically adjustable, wherein  $K_{HYB}$  is a hybrid path gain.

9. (PREVIOUSLY PRESENTED) The apparatus of claim 8 wherein the subscriber line impedance is approximated by series coupled resistor  $R_x$  and capacitor  $C_x$ , wherein the transfer function from the driver to the receive port to the output is  $K_{rx} \cdot \frac{1 + sC_x R_x}{1 + sC_x(R_x + 2R_D)} \cdot \frac{s}{s + HYB0}$ , wherein HYB0 is adjusted to have a value substantially equivalent to  $\frac{1}{R_x C_x}$ , wherein HYB0 is adjusted to substantially match  $Z(s)$ , wherein HYBP and KHYB are selected such that  $K_{HYB} \cdot \frac{s}{s + HYBP}$  is substantially the same as  $K_{rx} \cdot \frac{1 + sC_x R_x}{1 + sC_x(R_x + 2R_D)} \cdot \frac{s}{s + HYB0}$ .

10. (ORIGINAL) The apparatus of claim 6 wherein the hybrid network is tuned to behave substantially as a first order network.
11. (ORIGINAL) The apparatus of claim 6 wherein the hybrid network resides on an integrated circuit die.
12. (ORIGINAL) The apparatus of claim 11 wherein the hybrid network is a complementary metal oxide semiconductor integrated circuit.
13. (ORIGINAL) The apparatus of claim 6 wherein the upstream and downstream data signals are multitone modulated data signals.